



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Wendell P. Noble Jr.

Title: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT

Attorney Docket No.: 303.257US4

Customer No.: 21186

Confirmation No.:



PATENT APPLICATION TRANSMITTAL

MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
- ☒ **DIVISIONAL** of prior Patent Application No. (09/911688) (under 37 CFR 1.53(b)) comprising:
 - ☒ Specification (24 pgs, including claims numbered 1 through 34 and a 1 page Abstract).
 - ☒ Formal Drawing(s) (21 sheets).
 - ☒ Copy of signed Combined Declaration and Power of Attorney (3 pgs) from prior application.
 - ☒ Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
- ☒ Prior application is assigned of record to Micron Technology, Inc.
- ☒ Information Disclosure Statement (2 pgs), Form 1449 (1 pgs)
References NOT enclosed, cited in prior application.
- ☒ A Communication Concerning Related Applications (2 pgs.)

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	34-20	14	x 18.00 =	\$252.00
INDEPENDENT CLAIMS	7-3	4	x 84.00 =	\$336.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$750.00
TOTAL				\$1338.00

Please charge the required fee of \$1338.00 and any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

By: David R. Cochran
Atty: David R. Cochran
Reg. No. 46,632

"Express Mail" mailing label number: EV298566393US

Date of Deposit: September 19, 2003

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S/N N/A

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Wendell P. Noble Jr.	Examiner: Unknown
Serial No.:	Not Yet Assigned	Group Art Unit: Unknown
Filed:	Herewith	Docket: 303.257US4
Title:	METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT	

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
08/845609 5,892,707	April 25, 1997		
08/842950 6,190,960	April 25, 1997	303.256US1	METHOD FOR COUPLING TO SEMICONDUCTOR DEVICE IN AN INTEGRATED CIRCUIT HAVING EDGE-DEFINED, SUB-LITHOGRAPHIC CONDUCTORS
09/537602 6,194,262	March 29, 2000	303.256US2	METHOD FOR COUPLING TO SEMICONDUCTOR DEVICE IN AN INTEGRATED CIRCUIT HAVING EDGE-DEFINED, SUB-LITHOGRAPHIC CONDUCTORS

COMMUNICATION CONCERNING CO-PENDING APPLICATIONS

Serial Number: N/A

Filing Date: N/A

Title: METHOD FOR FORMING GATE SEGMENTS FOR AN INTEGRATED CIRCUIT

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Dkt: 303.257US4

09/779090
6,362,043

February 8, 303.256US3
2001

METHOD FOR COUPLING TO
SEMICONDUCTOR DEVICE IN AN
INTEGRATED CIRCUIT HAVING EDGE-
DEFINED, SUB-LITHOGRAPHIC
CONDUCTORS

Respectfully submitted,

WENDELL P. NOBLE JR.

By Applicant's Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH,
P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 371-2157

Date 19 September 2003 By David R. Cochran
David R. Cochran
Reg. No. 46,632

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